

# Vulkanised 2024

The 6<sup>th</sup> Vulkan Developer Conference  
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## Vulkan Video Encode APIs

Tony Zlatinski, NVIDIA



# Presentation Outline

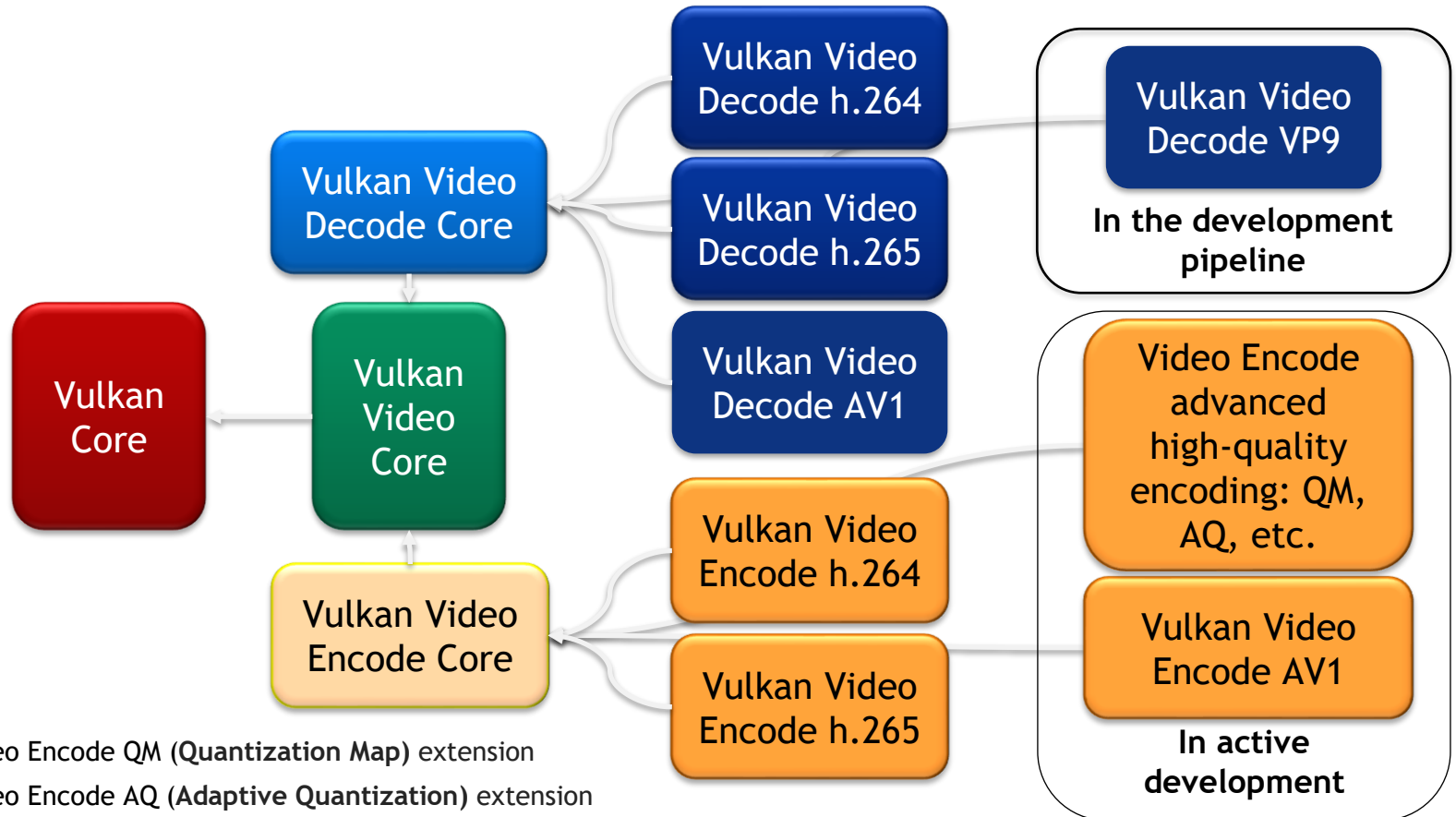
- Vulkan video roadmap
- General Vulkan video encode concepts and architecture
- Vulkan video encode quality and rate control
- Vulkan video tools



# Vulkan Video API Design Goals

- **Cross-platform and vendor-neutral low-level HW state(-less) video codecs API**
- **Closer integration with graphics, compute and display**
- **Improved low-level synchronization reducing the processing delays**
- **Support for scalable applications, from high-performance servers to energy-efficient devices with limited resources**
  - **An optimized solution for processing multiple video streams in parallel, efficiently distributing tasks across various CPU and hardware codec cores**
  - **Reduced latency and execution overhead during processing**
  - **Minimized overhead of CPU, GPU, ASIC, and memory resources**

# Vulkan Video Core and Codec Extensions



# AVC (H.264) and HEVC (H.265) and AV1 driver availability

Intel will support **H.264/5 video encode** and **AV1 video decode** through Vulkan Video extension for all Intel® Arc™ Graphics products. Please [download](#) the driver for H.264/5 video decoding support.

**AMD** supports Vulkan Video extensions on all RDNA™ architecture-based graphics with AMD Software: Adrenalin Edition™ drivers for Windows®. Please [download](#) the latest driver with production support for **H.264/5 decode** and BETA support for **H.264/5 encode** and **decode AV1**.

**NVIDIA** released Windows and Linux drivers available for **H.264/5 decode** and **encode** starting with Maxwell/Pascal and later (the latest one being ADA) GPUs and **AV1 decode** available for Ampere and later GPUs

The latest information about **Open-Source drivers** for Vulkan Video for **RADV/AMD** and **ANV/Intel** can be found on Dave Airlie's [blog](#).

# Vulkan Video Support

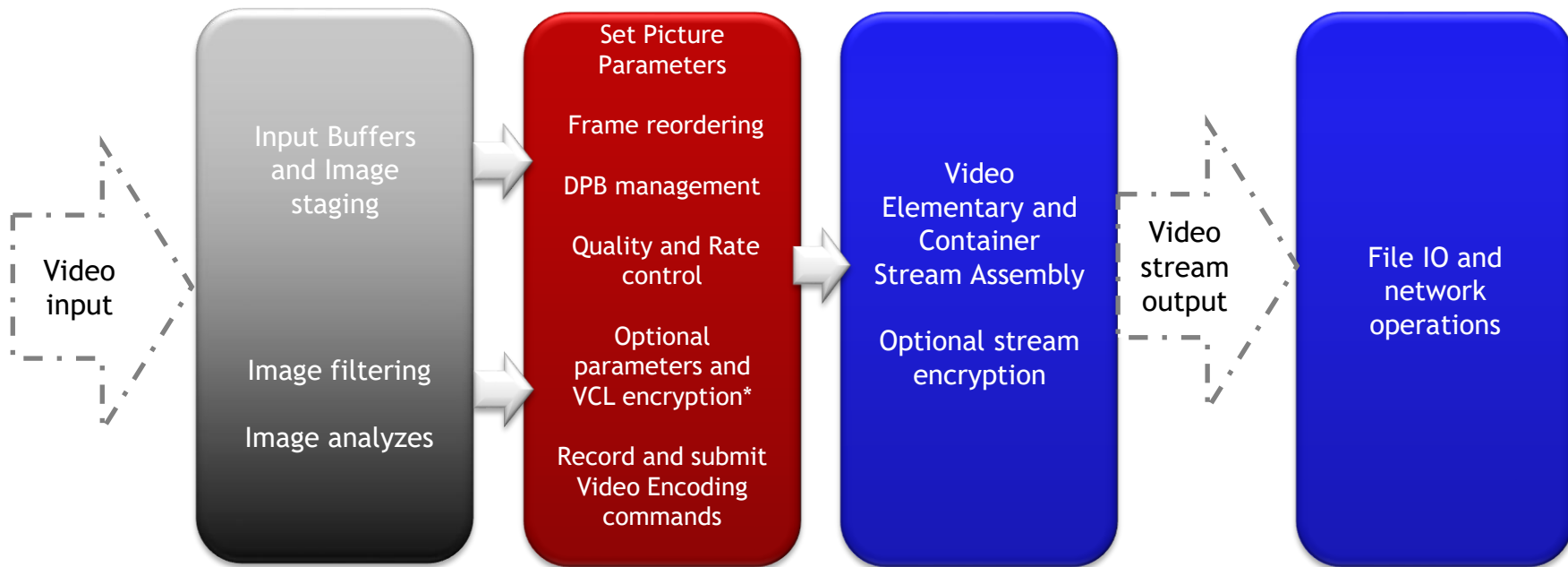
NVIDIA open-source [vk\\_video\\_decode\\_and\\_encode](#) sample

Early adoption by leading multimedia frameworks:

- [GStreamer](#) (including support by [Igalia](#) and [COLLABORA](#))
- [FFmpeg](#) (including development by [Lynne](#))

# Vulkan Video Encode Execution Model

# Video Encode Application Processing Pipeline



Vulkan HW accelerators and host (CPU) processing



Vulkan HW accelerators processing

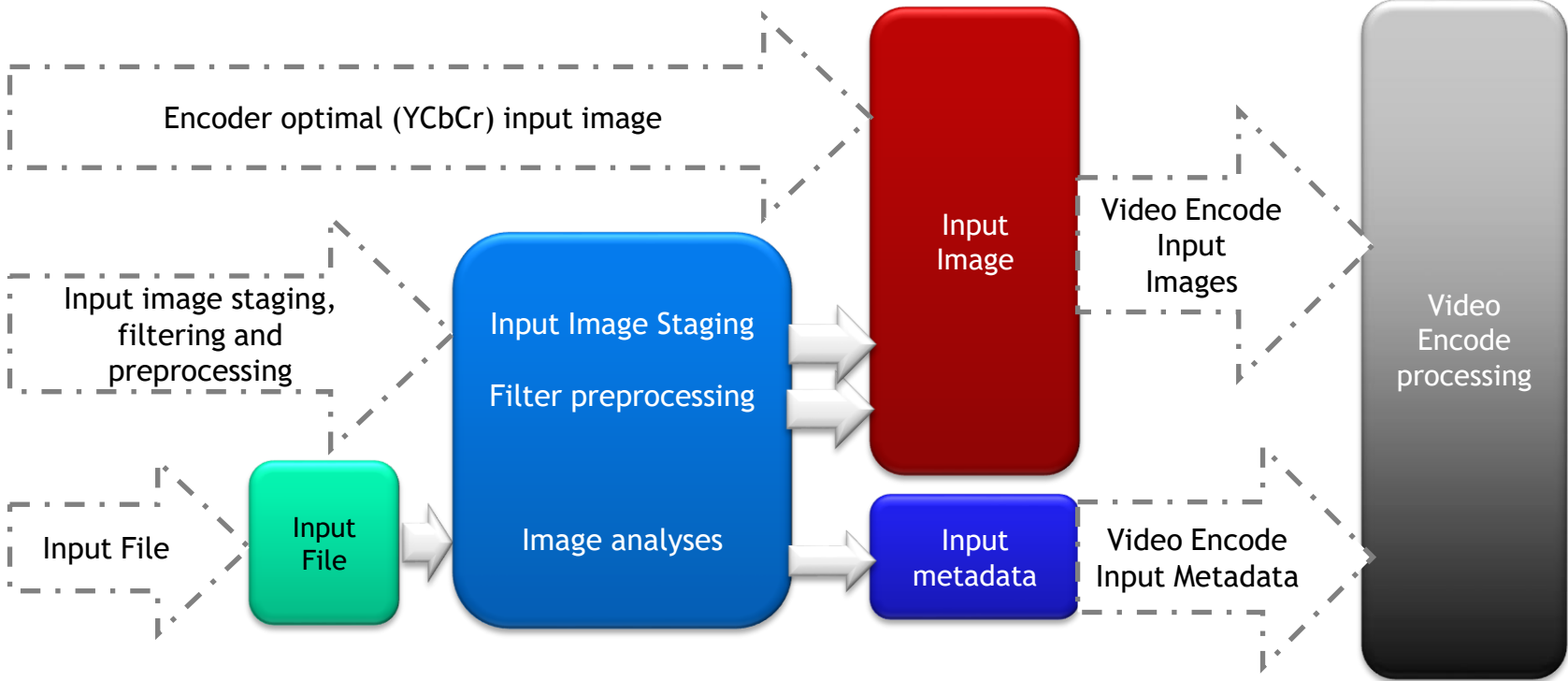


Host (CPU) application processing

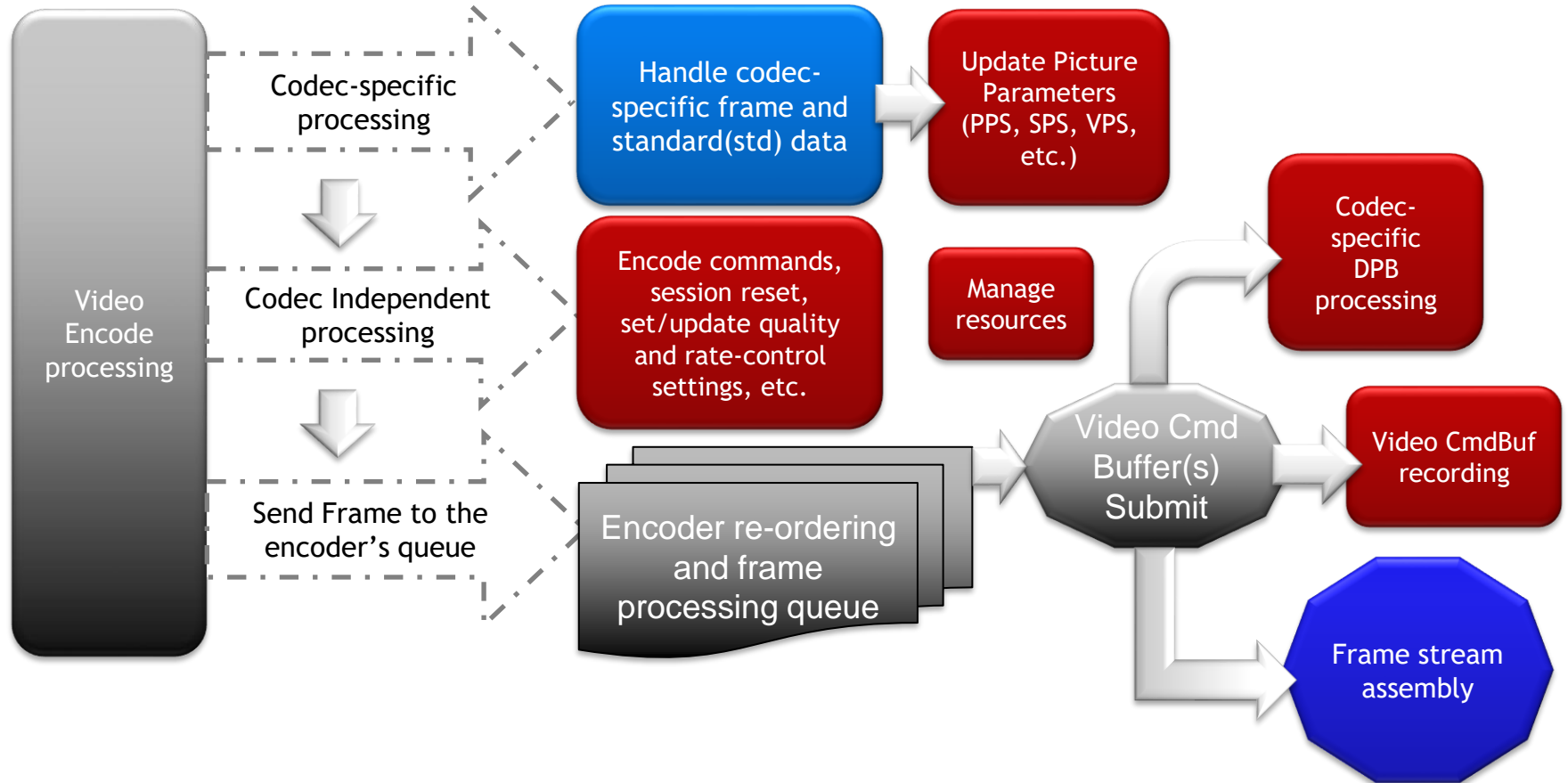
*Parameters and VCL encryption\* features are coming soon*



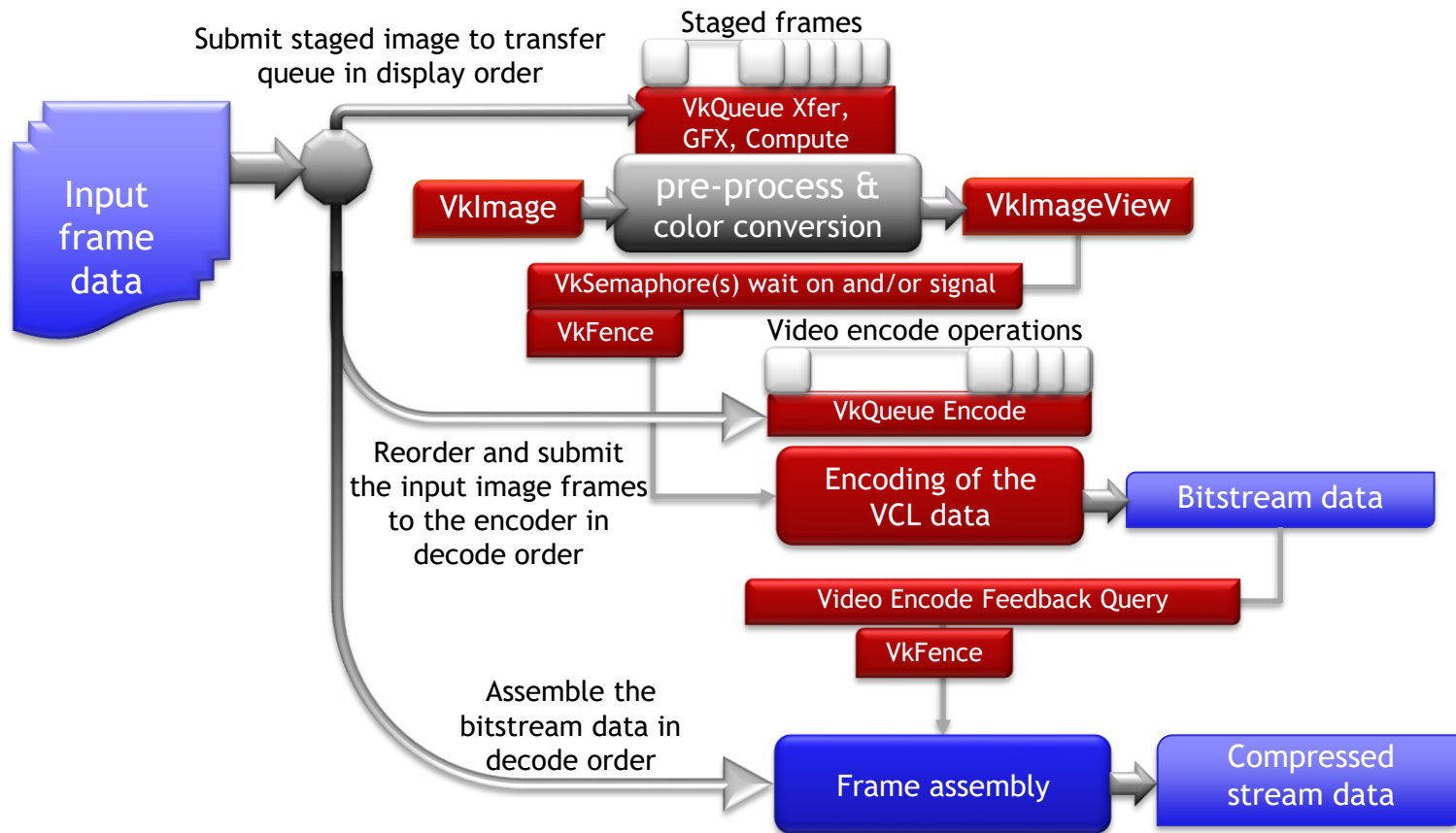
# Input Application Processing Pipeline



# Video Encode Application Processing Pipeline



# Interaction Between Input and Encoding Stages



# Before encoding the video sequence in Vulkan

- Create Vulkan Device with Video Encode Support Queue(s)
- Obtain the Video Encode Queue for submitting the work
- Query the Vulkan Video Physical device properties to gather the implementation requirements
- Create:
  - Input and DPB Vulkan image resources
  - Vulkan buffer resources for the compressed output bitstream
  - Video profile compatible with the targeted video stream format
  - Video session for the compressed stream
  - Video session parameters
  - Command buffers for recording the video commands
  - Command buffers for recording the staging and pre-processing of the video input
  - Synchronization primitives: semaphores, fences and queries
- Reset and configure the video session

# Vulkan Video Core Architecture Presentation

Vulkanised 2023 presentation on Vulkan Video Core  
**A Deep Dive into Vulkan Video**

[Video on YouTube](#) and [Slides](#)

# Video Encode Properties and Capabilities

Get the supported video encode capabilities by calling [vkGetPhysicalDeviceVideoCapabilitiesKHR](#) with a [VkVideoEncodeCapabilitiesKHR](#) structure chained with the [VkVideoCapabilitiesKHR](#).

For H.264, chain additionally [VkVideoEncodeH264CapabilitiesKHR](#).

For H.265, chain additionally [VkVideoEncodeH265CapabilitiesKHR](#).

[VkVideoEncodeCapabilitiesKHR](#) structure:

[VkVideoEncodeCapabilityFlagBitsKHR](#)

[VkVideoEncodeRateControlModeFlagBitsKHR](#)

uint32\_t

uint64\_t

uint32\_t

VkExtent2D

[VkVideoEncodeFeedbackFlagBitsKHR](#)

flags;

rateControlModes;

maxRateControlLayers;

maxBitrate;

maxQualityLevels;

encodeInputPictureGranularity;

supportedEncodeFeedbackFlags;

# Video Encode Parameter Overrides

[vkGetEncodedVideoSessionParametersKHR](#) and [optimizing overrides](#):

[VkVideoSessionCreateInfoKHR::VK\\_VIDEO\\_SESSION\\_CREATE\\_ALLOW\\_ENCODE\\_PARAMETER\\_OPTIMIZATIONS\\_BIT\\_KHR](#)

## [vkGetEncodedVideoSessionParametersKHR](#)

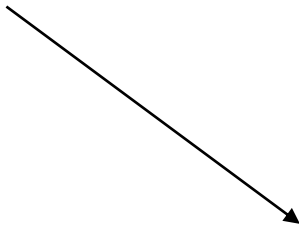
|  |                                     |
|--|-------------------------------------|
| <i>VkDevice</i>  | <i>device,</i>                      |
| <i>const <a href="#">VkVideoEncodeSessionParametersGetInfoKHR</a>*</i> | <i>pVideoSessionParametersInfo,</i> |
| <i><a href="#">VkVideoEncodeSessionParametersFeedbackInfoKHR</a>*</i>  | <i>pFeedbackInfo,</i>               |
| <i>size_t*</i>   | <i>pDataSize,</i>                   |
| <i>void*</i>   | <i>pData</i>                        |

## [VkVideoEncodeSessionParametersFeedbackInfoKHR](#)

*VkBool32*      *hasOverrides;*

## [VkVideoEncodeSessionParametersGetInfoKHR](#)

*VkVideoSessionParametersKHR*      *videoSessionParameters*



# Video Encode Feedback Queries

VkQueryPoolVideoEncodeFeedbackCreateInfoKHR

VkVideoEncodeFeedbackFlagBitsKHR:

VK\_VIDEO\_ENCODE\_FEEDBACK\_BITSTREAM\_BUFFER\_OFFSET\_BIT\_KHR

VK\_VIDEO\_ENCODE\_FEEDBACK\_BITSTREAM\_BYTES\_WRITTEN\_BIT\_KHR

VK\_VIDEO\_ENCODE\_FEEDBACK\_BITSTREAM\_HAS\_OVERRIDES\_BIT\_KHR

## Inline Queries

VK\_KHR\_video\_maintenance1 extension adds support for Inline Queries



# Vulkan video encode quality and rate control

# Encoder's Quality Level

Choose a [qualityLevel](#) value that must be less than [VkVideoEncodeCapabilitiesKHR::maxQualityLevels](#) returned by the implementation via [vkGetPhysicalDeviceVideoCapabilitiesKHR](#)

[VkVideoEncodeQualityLevelInfoKHR](#)  
uint32\_t [qualityLevel](#);

[VkVideoSessionParametersCreateInfoKHR](#)

specify the video encode quality level to use for a video session parameters object

[VkVideoCodingControlInfoKHR](#)

VK\_VIDEO\_CODING\_CONTROL\_ENCODE\_QUALITY\_LEVEL\_BIT\_KHR  
change the video encode quality level state of the bound video session.

# Quality usage flags from [VkVideoEncodeUsageInfoKHR](#)

- [VkVideoEncodeUsageFlagBitsKHR](#)

VK\_VIDEO\_ENCODE\_USAGE\_TRANSCODING\_BIT\_KHR  
VK\_VIDEO\_ENCODE\_USAGE\_STREAMING\_BIT\_KHR  
VK\_VIDEO\_ENCODE\_USAGE\_RECORDING\_BIT\_KHR  
VK\_VIDEO\_ENCODE\_USAGE\_CONFERENCING\_BIT\_KHR

- [VkVideoEncodeContentFlagBitsKHR](#)

VK\_VIDEO\_ENCODE\_CONTENT\_CAMERA\_BIT\_KHR  
VK\_VIDEO\_ENCODE\_CONTENT\_DESKTOP\_BIT\_KHR  
VK\_VIDEO\_ENCODE\_CONTENT\_RENDERED\_BIT\_KHR

- [VkVideoEncodeTuningModeKHR](#)

VK\_VIDEO\_ENCODE\_TUNING\_MODE\_DEFAULT\_KHR  
VK\_VIDEO\_ENCODE\_TUNING\_MODE\_HIGH\_QUALITY\_KHR  
VK\_VIDEO\_ENCODE\_TUNING\_MODE\_LOW\_LATENCY\_KHR  
VK\_VIDEO\_ENCODE\_TUNING\_MODE\_ULTRA\_LOW\_LATENCY\_KHR  
VK\_VIDEO\_ENCODE\_TUNING\_MODE\_LOSSLESS\_KHR

[VkVideoEncodeUsageInfoKHR](#) is  
chained to [VkVideoProfileInfoKHR](#)

# Video Encode Rate Control

- Rate Control modes
  - Explicit, by disabling the rate control and allowing the application to specify per-operation rate control parameters controlling the encoding quality via the QP value specified in constantQp of the corresponding codec slice/segment/tile
  - Constant bitrate (CBR) rate control
  - Variable bitrate (VBR) rate control
  - Implementation-specific rate control
- Rate Control State

[VkVideoBeginCodingInfoKHR](#)

for the [video coding scope](#)

[VkVideoCodingControlInfoKHR](#)

command against the bound video session



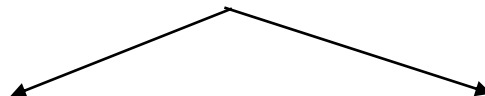
[VkVideoEncodeRateControlInfoKHR](#)

# Control Layout with Rate Control Disabled

[vkCmdEncodeVideoKHR](#)



[VkVideoEncodeInfoKHR](#)



H.265:

[VkVideoEncodeH265PictureInfoKHR](#)

```
uint32_t          naluSliceSegmentEntryCount;  
const VkVideoEncodeH265NaluSliceSegmentInfoKHR*  
                pNaluSliceSegmentEntries;
```



H.265:

[VkVideoEncodeH265NaluSliceSegmentInfoKHR](#)

```
int32_t          constantQp;  
const StdVideoEncodeH265SliceSegmentHeader*  
                pStdSliceSegmentHeader;
```

H.264:

[VkVideoEncodeH264PictureInfoKHR](#)

```
uint32_t          naluSliceEntryCount;  
const VkVideoEncodeH264NaluSliceInfoKHR*  
                pNaluSliceEntries;
```



H.264:

[VkVideoEncodeH264NaluSliceInfoKHR](#)

```
int32_t          constantQp;  
const StdVideoEncodeH264SliceHeader*  
                pStdSliceHeader;
```

# Control layout case with rate control enabled

[vkCmdControlVideoCodingKHR](#) VK\_VIDEO\_CODING\_CONTROL\_ENCODE\_RATE\_CONTROL\_BIT\_KHR

[VkVideoEncodeRateControlInfoKHR](#):

[VkVideoEncodeRateControlFlagsKHR](#)

[VkVideoEncodeRateControlModeFlagBitsKHR](#)

uint32\_t

const [VkVideoEncodeRateControlLayerInfoKHR](#) \*

uint32\_t

uint32\_t

flags;

rateControlMode;

layerCount;

pLayers;

virtualBufferSizeInMs;

initialVirtualBufferSizeInMs;

[VkVideoEncodeRateControlLayerInfoKHR](#)

uint64\_t

averageBitrate;

uint64\_t

maxBitrate;

uint32\_t

frameRateNumerator;

uint32\_t

frameRateDenominator;

[VkVideoEncodeH26XRateControlLayerInfoKHR](#)

[VkVideoEncodeH26XQpKHR](#)

VkBool32

[VkVideoEncodeH26XQpKHR](#)

VkBool32

[VkVideoEncodeH26XFrameSizeKHR](#)

minQp;

useMaxQp;

maxQp;

useMaxFrameSize;

maxFrameSize;

[VkVideoEncodeH26XQpKHR](#)

int32\_t qpI;

int32\_t qpP;

int32\_t qpB;

[VkVideoEncodeH26XFrameSizeKHR](#)

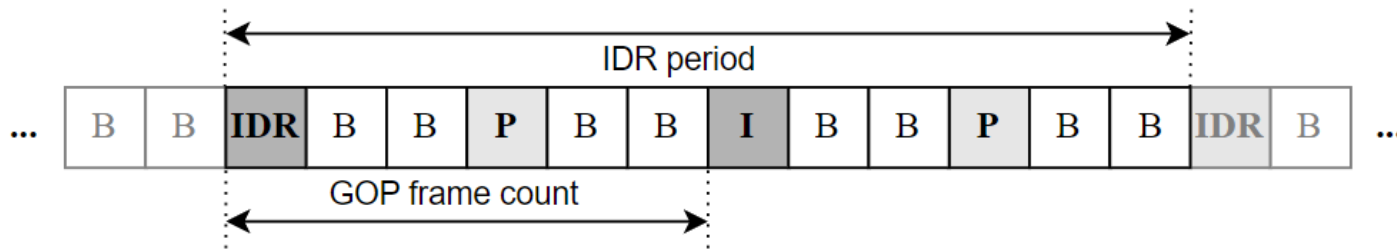
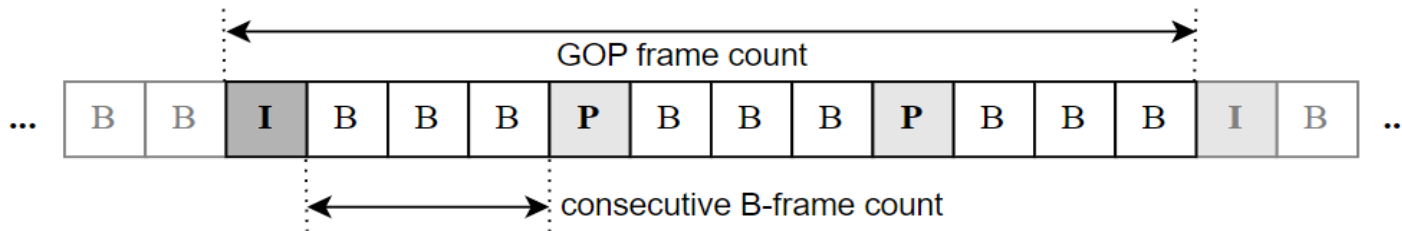
uint32\_t frameISize;

uint32\_t framePSize;

uint32\_t frameBSize;

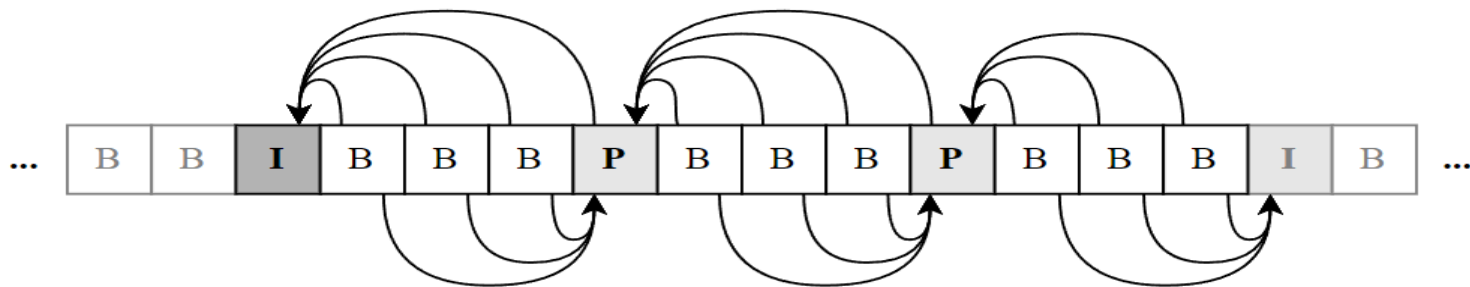
# Specifying the GOP structure

gopFrameCount  
idrPeriod  
consecutiveBFrameCount  
temporalLayerCount



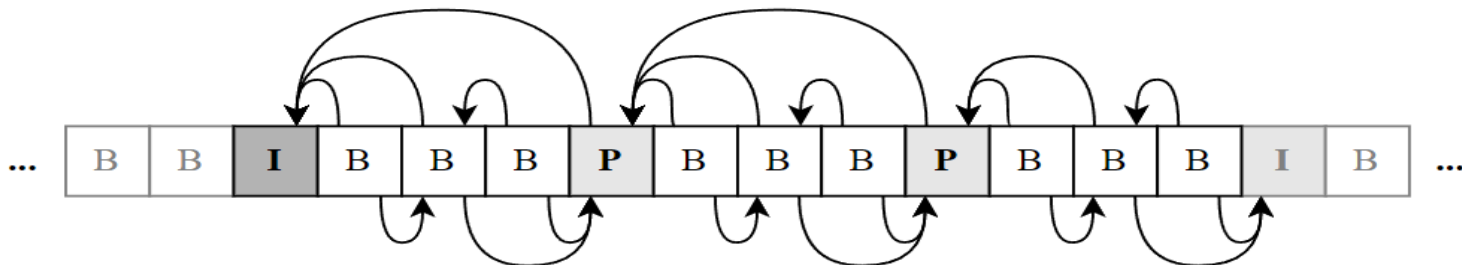
[VkVideoEncodeH26XRateControlFlagBitsKHR:VK\\_VIDEO\\_ENCODE\\_H26X\\_RATE\\_CONTROL\\_REGULAR\\_GOP\\_BIT\\_KHR](#)

# Vulkan Video API Supported GOP Patterns



Flat Reference Pattern

[VkVideoEncodeH26XRateControlFlagBitsKHR:VK\\_VIDEO\\_ENCODE\\_H26X\\_RATE\\_CONTROL\\_REFERENCE\\_PATTERN\\_FLAT\\_BIT\\_KHR](#)



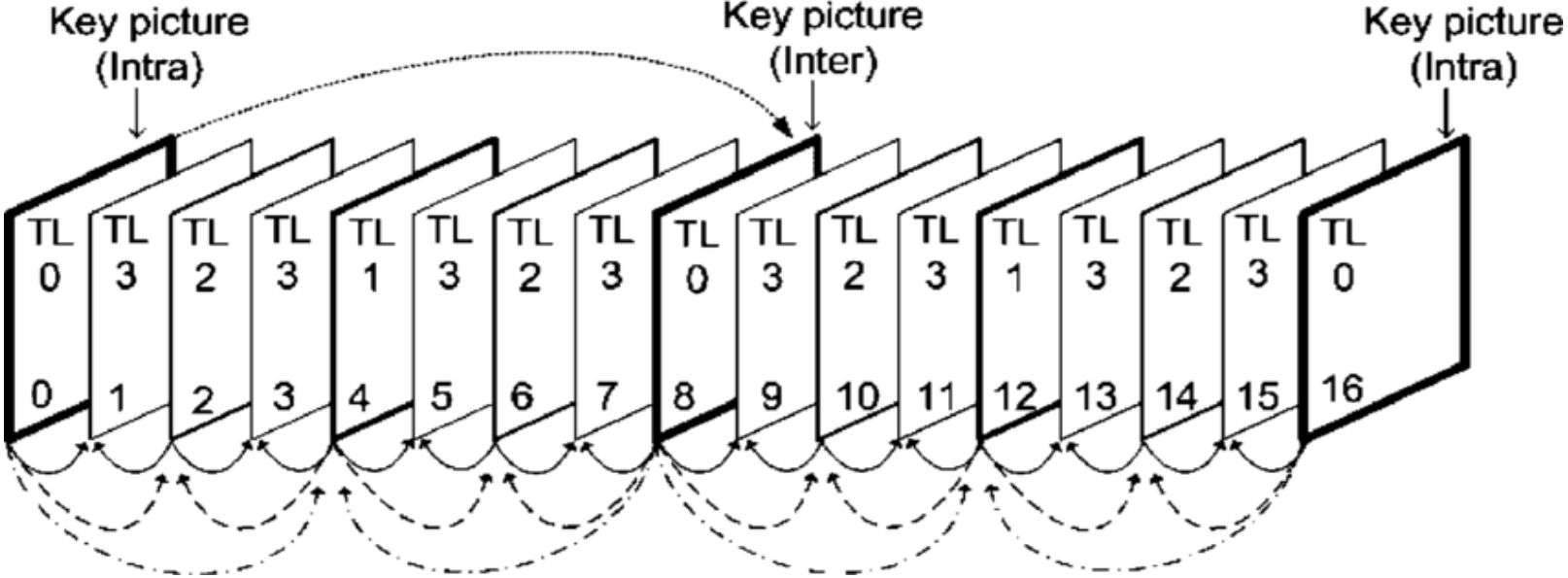
Dyadic Reference Pattern

[VkVideoEncodeH26XRateControlFlagBitsKHR:VK\\_VIDEO\\_ENCODE\\_H26X\\_RATE\\_CONTROL\\_REFERENCE\\_PATTERN\\_DYADIC\\_BIT\\_KHR](#)

[VkVideoEncodeH264RateControlInfoKHR::temporalLayerCount = 1;](#) [VkVideoEncodeH265RateControlInfoKHR::subLayerCount = 0](#)



# Dyadic Temporal Layer Pattern



[VkVideoEncodeH26XRateControlFlagBitsKHR:VK\\_VIDEO\\_ENCODE\\_H26X\\_RATE\\_CONTROL\\_TEMPORAL\\_SUB\\_LAYER\\_PATTERN\\_DYADIC\\_BIT\\_KHR](#)

[VkVideoEncodeH264RateControlInfoKHR::temporalLayerCount = 4;](#) [VkVideoEncodeH265RateControlInfoKHR ::subLayerCount = 4](#)

Picture Source:  
[https://www.researchgate.net/figure/Hierarchical-prediction-structure-for-SVC-for-a-GOP-size-of-8\\_fig1\\_23676483](https://www.researchgate.net/figure/Hierarchical-prediction-structure-for-SVC-for-a-GOP-size-of-8_fig1_23676483)

# Getting Codec-specific Quality Level Properties

[vkGetPhysicalDeviceVideoEncodeQualityLevelPropertiesKHR\(\)](#)

[VkPhysicalDeviceVideoEncodeQualityLevelInfoKHR](#) \* pQualityLevelInfo  
[VkVideoEncodeQualityLevelPropertiesKHR](#)\* pQualityLevelProperties

[VkPhysicalDeviceVideoEncodeQualityLevelInfoKHR](#) :

const [VkVideoProfileInfoKHR](#) \* pVideoProfile;  
 uint32\_t [qualityLevel](#);

[VkVideoEncodeQualityLevelPropertiesKHR](#):

[VkVideoEncodeRateControlModeFlagBitsKHR](#) preferredRateControlMode;  
 uint32\_t preferredRateControlLayerCount;

[VkVideoEncodeUsageInfoKHR](#):

[VkVideoEncodeUsageFlagBitsKHR](#) videoUsageHints;  
[VkVideoEncodeContentFlagBitsKHR](#) videoContentHints;  
[VkVideoEncodeTuningModeKHR](#) tuningMode;

H.264

[VkVideoEncodeH264QualityLevelPropertiesKHR](#)  
[VkVideoEncodeH264RateControlFlagBitsKHR](#)

uint32\_t preferredRateControlFlags;  
 uint32\_t preferredGopFrameCount;  
 uint32\_t preferredIldrPeriod;  
 uint32\_t preferredConsecutiveBFrameCount;  
 uint32\_t preferredTemporalLayerCount;  
 VkVideoEncodeH264QpKHR preferredConstantQp;  
 uint32\_t preferredMaxL0ReferenceCount;  
 uint32\_t preferredMaxL1ReferenceCount;  
 VkBool32 preferredStdEntropyCodingModeFlag;

H.265

[VkVideoEncodeH265QualityLevelPropertiesKHR](#):  
[VkVideoEncodeH265RateControlFlagBitsKHR](#)

uint32\_t preferredRateControlFlags;  
 uint32\_t preferredGopFrameCount;  
 uint32\_t preferredIldrPeriod;  
 uint32\_t preferredConsecutiveBFrameCount;  
 uint32\_t preferredSubLayerCount;  
 VkVideoEncodeH265QpKHR preferredConstantQp;  
 uint32\_t preferredMaxL0ReferenceCount;  
 uint32\_t preferredMaxL1ReferenceCount;

# Selecting Supported Optimal GOP Patterns

## H.264

### [VkVideoEncodeH264CapabilitiesKHR](#)

```
...
uint32_t    maxPPictureL0ReferenceCount;
uint32_t    maxBPictureL0ReferenceCount;
uint32_t    maxL1ReferenceCount;
uint32_t    maxSubLayerCount;
VkBool32    expectDyadicTemporalSubLayerPattern;
int32_t     minQp;
int32_t     maxQp;
VkBool32    prefersGopRemainingFrames;
VkBool32    requiresGopRemainingFrames;
...
```

### [VkVideoEncodeH264QualityLevelPropertiesKHR](#)

#### [VkVideoEncodeH264RateControlFlagBitsKHR](#)

```
uint32_t    preferredRateControlFlags;
uint32_t    preferredGopFrameCount;
uint32_t    preferredIldrPeriod;
uint32_t    preferredConsecutiveBFrameCount;
uint32_t    preferredTemporalLayerCount;
VkVideoEncodeH264QpKHR    preferredConstantQp;
uint32_t    preferredMaxL0ReferenceCount;
uint32_t    preferredMaxL1ReferenceCount;
...
```

## H.265

### [VkVideoEncodeH265CapabilitiesKHR](#)

```
...
uint32_t    maxPPictureL0ReferenceCount;
uint32_t    maxBPictureL0ReferenceCount;
uint32_t    maxL1ReferenceCount;
uint32_t    maxSubLayerCount;
VkBool32    expectDyadicTemporalSubLayerPattern;
int32_t     minQp;
int32_t     maxQp;
VkBool32    prefersGopRemainingFrames;
VkBool32    requiresGopRemainingFrames;
...
```

### [VkVideoEncodeH265QualityLevelPropertiesKHR:](#)

#### [VkVideoEncodeH265RateControlFlagBitsKHR](#)

```
uint32_t    preferredRateControlFlags;
uint32_t    preferredGopFrameCount;
uint32_t    preferredIldrPeriod;
uint32_t    preferredConsecutiveBFrameCount;
uint32_t    preferredSubLayerCount;
VkVideoEncodeH265QpKHR    preferredConstantQp;
uint32_t    preferredMaxL0ReferenceCount;
uint32_t    preferredMaxL1ReferenceCount;
...
```

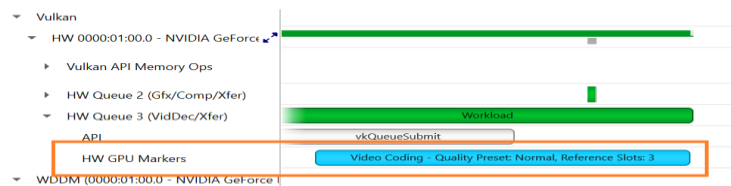
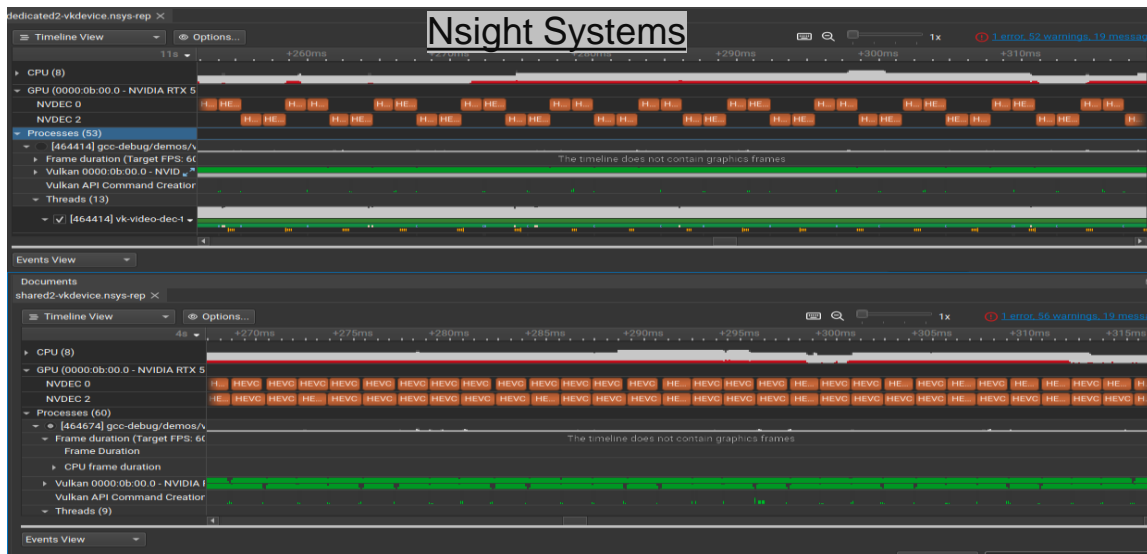
# Development tools

# Vulkan Video Frame Timing Using NVIDIA Nsight Graphics and Systems profiler

Check out [Optimizing Vulkan 1.3 Applications with Nsight Graphics and Nsight Systems](#) and [Nsight Systems - Vulkan Trace](#).

[NVIDIA Nsight Developer Tools](#) are a collection of debuggers, profilers, and optimizers that support performance tuning for applications using many graphics APIs, including Vulkan.

The [NVIDIA Nsight Systems](#) profiler will enable parsing of the specific workload of a Vulkan Video decoding queue, providing insights into processing bottlenecks within the context of the application.



# Vulkan Video Conclusions

- A robust cross-platform API for video acceleration
- Extensive flexibility for creating scalable, high-performance, and highly customizable video applications
- Benefits support from open-source community and leading Independent Hardware Vendors (IHV)

# Reference Materials

- [An Introduction to Vulkan Video](#)
- [A deep dive into Vulkan Video Presentation and Slides Vulkan Specification, including Vulkan video extensions](#)
- [Vulkan Headers](#)
- [Validation Layer PR](#)
- [Vulkan SDK including Vulkan video support and validation layers](#)
- [Vulkan video samples](#)
- [Optimizing Vulkan 1.3 Applications with Nsight Graphics and Nsight Systems and Nsight Systems - Vulkan Trace.](#)
- [NVIDIA beta Vulkan drivers](#)

# The Conference for the Era of AI and the Metaverse

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We explore what's driving transformation in everything from collaborative virtual worlds and advanced graphics to data science, healthcare, and beyond.

Come experience GTC for groundbreaking content, expert-led sessions, and a must-see keynote to accelerate your life's work.

March 17-21 | <http://www.nvidia.com/gtc/>

## Recommended Sessions and Training

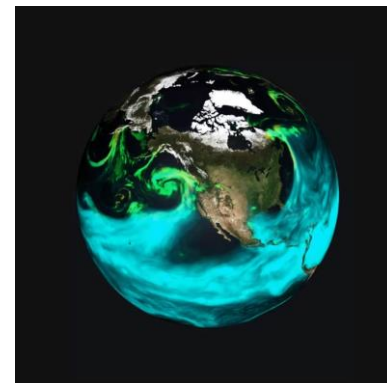
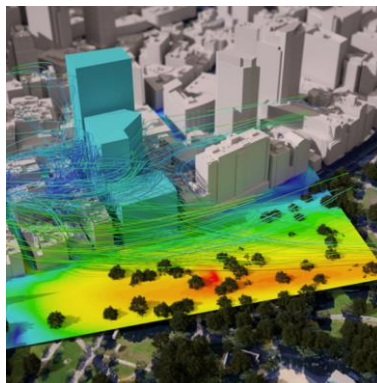
[Watch Party: Accelerating Ray Tracing and AI in Unreal Engine \[WP51851\]](#)

[Connect with the Experts: Using NVIDIA Developer Tools to Optimize Ray Tracing \[CWES52009\]](#)

[Ray-Tracing Development using NVIDIA Nsight Graphics and NVIDIA Nsight Systems\\* \[DLIT51580\]](#)

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# Q & A



**Thank you!**

**Questions?**